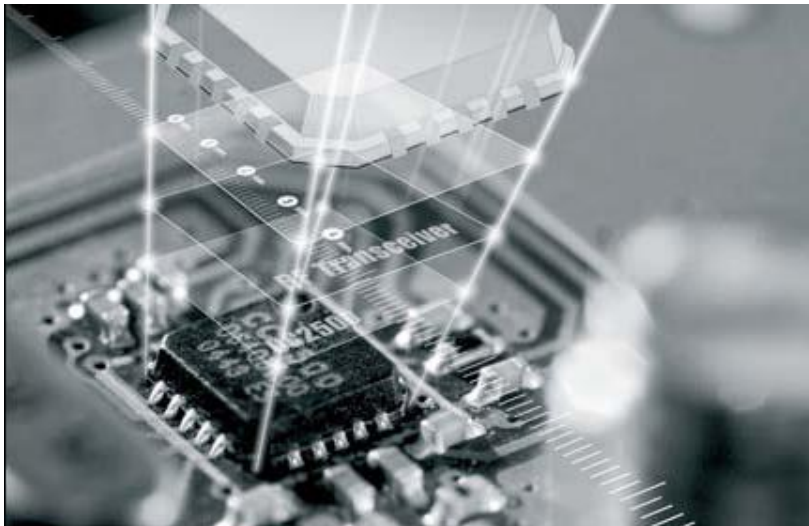




SPECIFICATION SPECIFICATION

2.4-GHz Wireless System-on-Module



Model : **2.4GHz RF Module**
Part No : TC26x0-F128-02-XX
Version : V3.1
Date : 2016.05.27

■ Applications

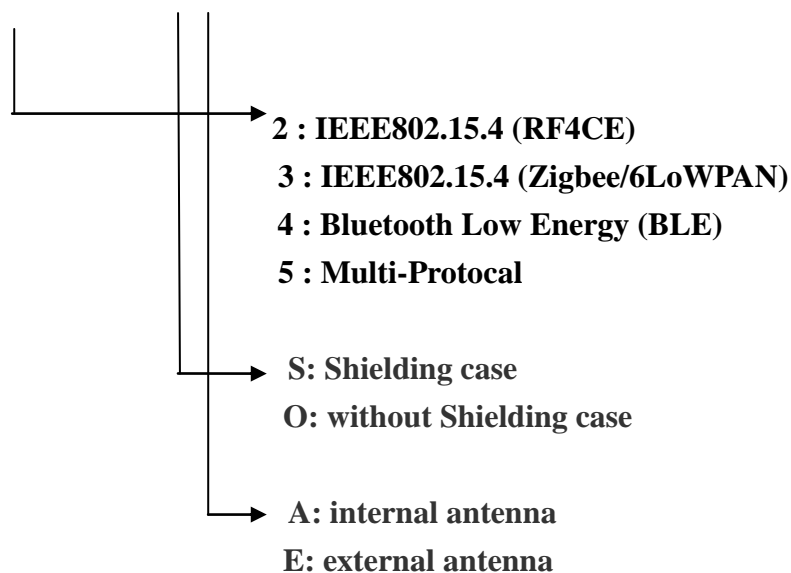
- Home and Building Automation
 - Connected Appliances
 - Lighting
 - Locks
 - Gateways
 - Security Systems
- Industrial
 - Logistics
 - Production and Manufacturing
 - Automation
 - Asset Tracking and Management
 - Remote Display
 - Cable Replacement
 - HMI
 - Access Control
- Retail
 - Beacons
 - Advertising
 - ESL / Price Tags
 - Point of Sales / Payment Systems
- Health and Medical
 - Thermometers
 - SpO2
 - Blood Glucose and Pressure Meters
 - Weight-scales
 - Vitals Monitoring
 - Hearing Aids
- Sports and Fitness
 - Activity Monitors and Fitness Trackers
 - Heart Rate Monitors
 - Running Sensors
 - Biking Sensors
 - Sports Watches
 - Gym Equipment
 - Team Sports Equipment
- HID
 - Remote Controls
 - Keyboards and Mice
 - Gaming
- Accessories
 - Toys
 - Trackers
 - Luggage-tags
 - Wearables

■ Selection Guide

Denomination : 2.4GHz Bluetooth RF Module

Part No. :

TC26x0-F128-02-XX



■ Absolute Maximum Ratings

		MIN	MAX	UNIT
Supply voltage, VDD _S ⁽³⁾	VDDR supplied by internal DC/DC regulator or internal GLDO	-0.3	4.1	V
Supply voltage, VDD _S ⁽³⁾ and VDDR	External regulator mode (VDD _S and VDDR pins connected on PCB)	-0.3	2.25	V
Voltage on any digital pin ⁽⁴⁾		-0.3	VDD _S +0.3, max 4.1	V
Voltage on crystal oscillator pins, X32K_Q1, X32K_Q2, X24M_N and X24M_P		-0.3	VDDR+0.3, max 2.25	V
Voltage on ADC input (V _{in})	Internal fixed or relative reference, voltage scaling enabled	-0.3	VDD _S	V
	Internal fixed reference, voltage scaling disabled	-0.3	1.49	
	Internal relative reference, voltage scaling disabled	-0.3	VDD _S / 2.9	
	External reference, voltage scaling enabled	-0.3	min (V _{ref} × 2.9, VDD _S)	
	External reference, voltage scaling disabled	-0.3	V _{ref}	
Voltage on external ADC reference (V _{ref})		-0.3	1.6	V
Input RF level			+5	dBm
T _{stg}	Storage temperature	-40	150	°C

- (1) All voltage values are with respect to VDD_S, unless otherwise noted.
- (2) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (3) VDD_{S2} and VDD_{S3} needs to be at the same potential as VDD_S.
- (4) Including analog capable DIO.

■ Recommended Operation Condition

		MIN	MAX	UNIT
Ambient temperature range		-40	85	°C
Operating supply voltage (VDD _S and VDDR), external regulator mode	For operation in 1.8 V systems (VDD _S and VDDR pins connected on PCB, internal DC/DC cannot be used)	1.7	1.95	V
Operating supply voltage (VDD _S)	For operation in battery-powered and 3.3 V systems (internal DC/DC can be used to minimize power consumption)	1.8	3.8	V

■ Electrical Specifications

● Current Consumption

TA = 25°C and VDD = 3 V

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT	
I _{core}	Core current consumption	Reset. RESET_N pin asserted		100		nA	
		Shutdown. No clocks running, no retention		150			
		Standby. With RTC, CPU, RAM and (partial) register retention. RCOSC_LF			1		μA
		Standby. With RTC, CPU, RAM and (partial) register retention. XOSC_LF			1.2		
		Standby. With Cache, RTC, CPU, RAM and (partial) register retention. RCOSC_LF			2.5		
		Standby. With Cache, RTC, CPU, RAM and (partial) register retention. XOSC_LF			2.7		
		Idle. Supply Systems and RAM powered.			550		
		Active. Core running CoreMark			1.45 mA + 31 μA/MHz		
		Radio RX ⁽¹⁾			5.9		mA
		Radio RX ⁽²⁾			6.1		
		Radio TX, 0 dBm output power ⁽¹⁾			6.1		
Radio TX, 5 dBm output power ⁽²⁾			9.1				
I _{peri}	Peripheral Current Consumption (Adds to core current I _{core} for each peripheral unit activated) ⁽³⁾						
	Peripheral power domain	Delta current with domain enabled		20		μA	
	Serial power domain	Delta current with domain enabled		13		μA	
	RF Core	Delta current with power domain enabled, clock enabled, RF Core Idle		237		μA	
	μDMA	Delta current with clock enabled, module idle		130		μA	
	Timers	Delta current with clock enabled, module idle		113		μA	
	I ² C	Delta current with clock enabled, module idle		12		μA	
	I2S	Delta current with clock enabled, module idle		36		μA	
	SSI	Delta current with clock enabled, module idle		93		μA	
	UART	Delta current with clock enabled, module idle		164		μA	

■ General Characteristics

TA = 25°C and VDD = 3 V, unless otherwise noted.

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Wake-up and Timing					
Idle -> Active			14		μs
Standby -> Active			151		μs
Shutdown -> Active			1015		μs
Flash Memory					
Supported flash erase cycles before failure		100			k Cycles
Flash page/sector erase current	Average delta current		12.6		mA
Flash page/sector erase time ⁽¹⁾			8		ms
Flash page/sector size			4		KB
Flash write current	Average delta current, 4 bytes at a time		8.15		mA
Flash write time ⁽¹⁾	4 bytes at a time		8		μs

■ RF Characteristics

RX Sensitivity

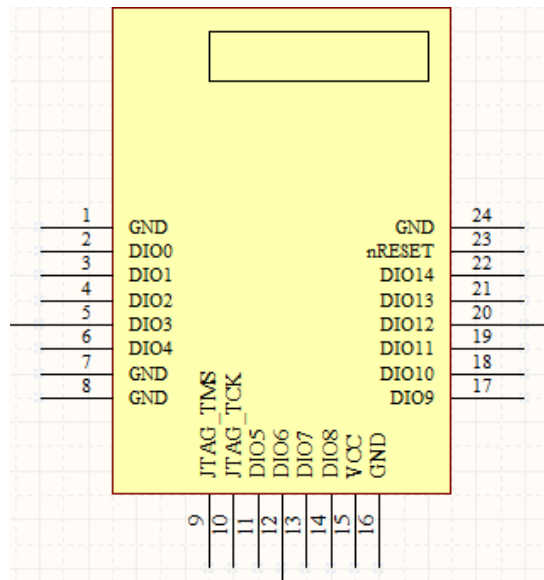
1Mbps, GFSK, 250-KHz deviation, Bluetooth low energy mode and 1%BER

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Receiver sensitivity	Differential mode. Measured at the SMA		-97		dBm

TX output Power

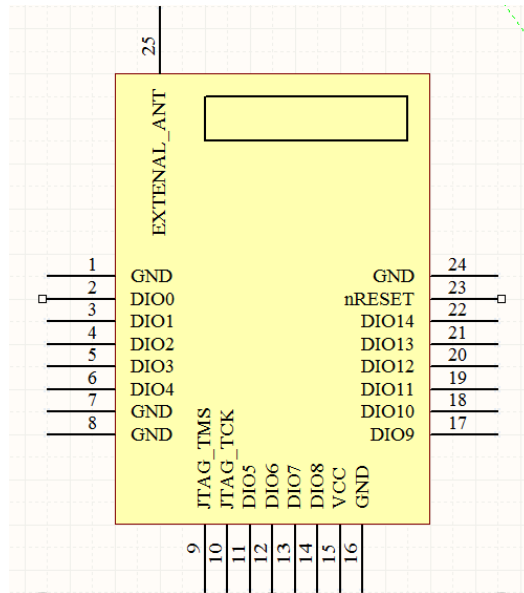
Output power, highest setting	Measured on 50-Ω load, delivered to a single-ended		+2		dBm
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TC26x0-F128-02-XA RF Module (internal antenna) Pin Configuration



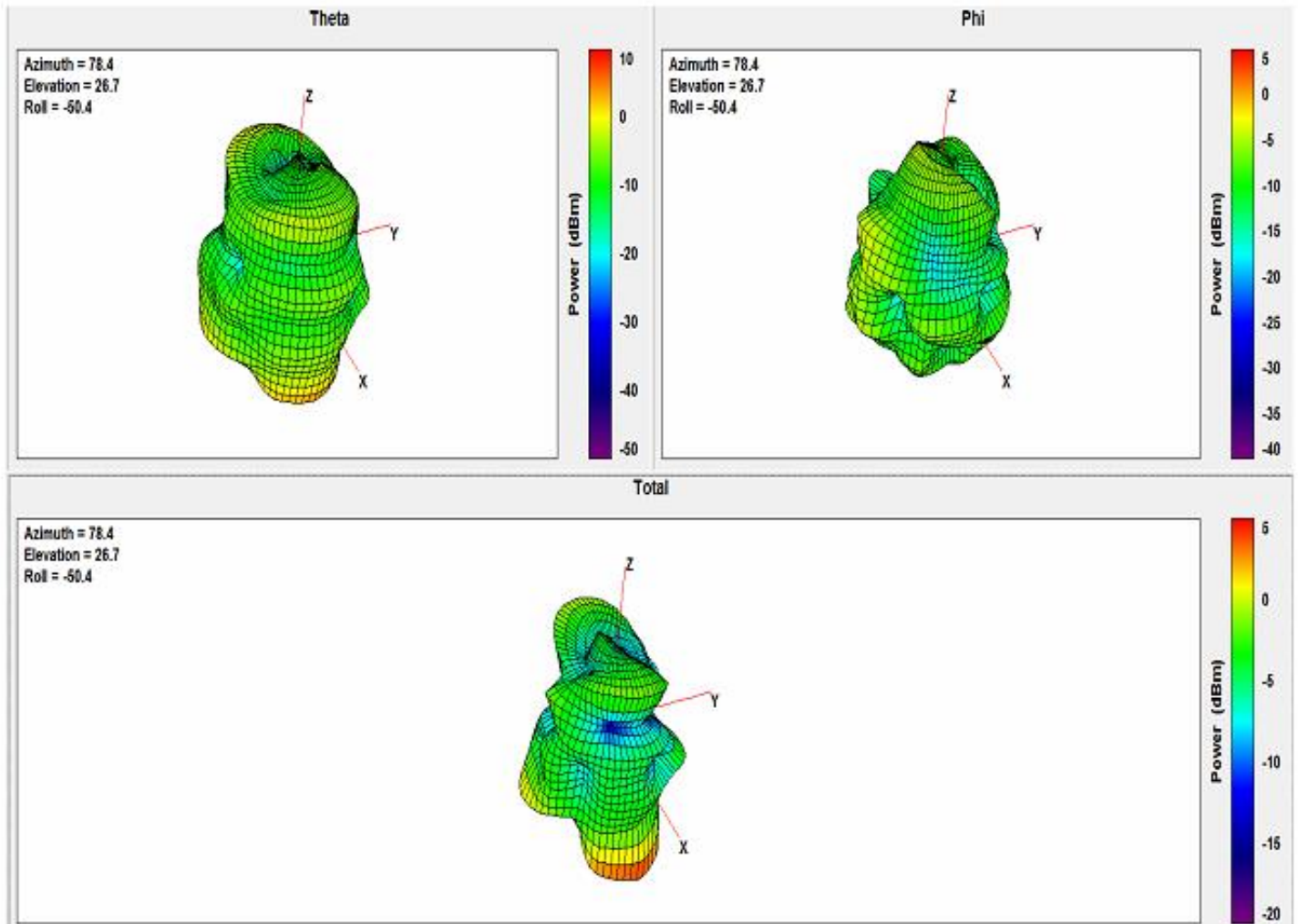
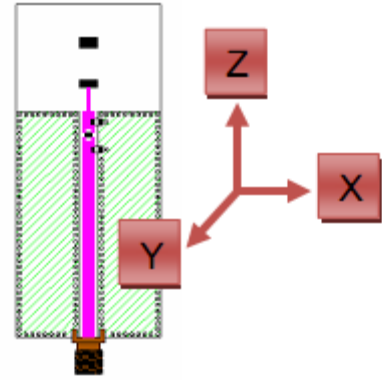
Pin#	Pin Define	Pin Type	Description
1	GND	GND	Ground
2	DIO_0	Digital I/O	
3	DIO_1	Digital I/O	
4	DIO_2	Digital I/O	
5	DIO_3	Digital I/O	
6	DIO_4	Digital I/O	
7	GND	Digital I/O	
8	GND	Digital I/O	
9	TMS	Digital I/O	JTAG TMSC
10	TCK	Digital I/O	JTAG TCKC
11	DIO_5	Digital I/O	
12	DIO_6	Digital I/O	
13	DIO_7	Digital/Analog I/O	
14	DIO_8	Digital/Analog I/O	
15	VDD	POWER	2~3.6V power supply
16	GND	GND	Ground
17	DIO_9	Digital/Analog I/O	
18	DIO_10	Digital/Analog I/O	
19	DIO_11	Digital/Analog I/O	
20	DIO_12	Digital/Analog I/O	
21	DIO_13	Digital/Analog I/O	
22	DIO_14	Digital/Analog I/O	
23	NRESET	RESET	RESET
24	GND	GND	Ground

TC26x0-F128-02-XE RF Module (External antenna) Pin Configuration



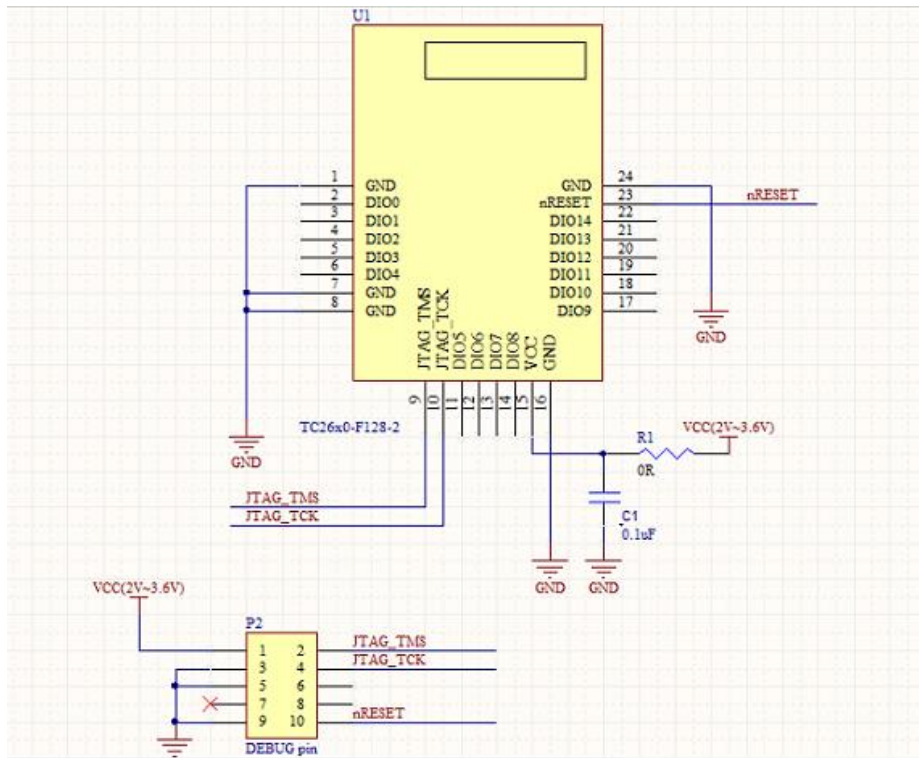
Pin#	Pin Define	Pin Type	Description
1	GND	GND	Ground
2	DIO_0	Digital I/O	
3	DIO_1	Digital I/O	
4	DIO_2	Digital I/O	
5	DIO_3	Digital I/O	
6	DIO_4	Digital I/O	
7	GND	Digital I/O	
8	GND	Digital I/O	
9	TMS	Digital I/O	JTAG TMSC
10	TCK	Digital I/O	JTAG TCKC
11	DIO_5	Digital I/O	
12	DIO_6	Digital I/O	
13	DIO_7	Digital/Analog I/O	
14	DIO_8	Digital/Analog I/O	
15	VDD	POWER	2~3.6V power supply
16	GND	GND	Ground
17	DIO_9	Digital/Analog I/O	
18	DIO_10	Digital/Analog I/O	
19	DIO_11	Digital/Analog I/O	
20	DIO_12	Digital/Analog I/O	
21	DIO_13	Digital/Analog I/O	
22	DIO_14	Digital/Analog I/O	
23	NRESET	RESET	RESET
24	GND	GND	Ground
25	ANT	Analog	Antenna feed point

Antenna Radiation Pattern

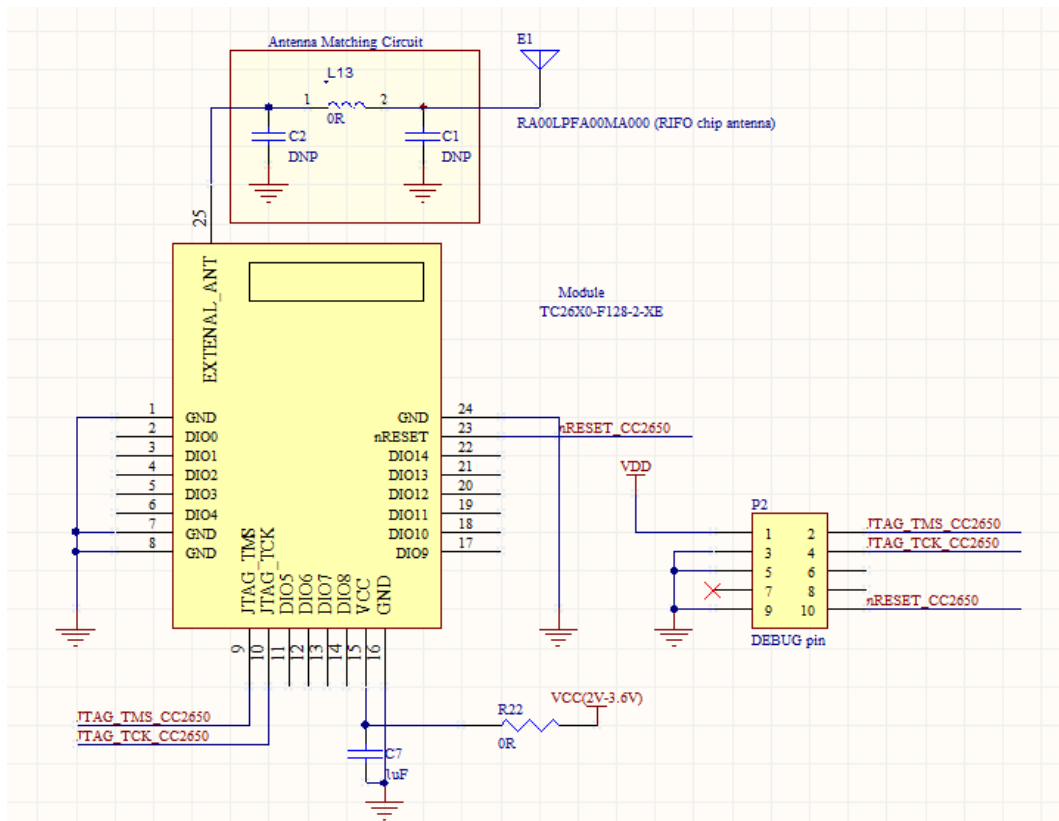


TC26x0-F128-02-XX RF Module Example Design schematic

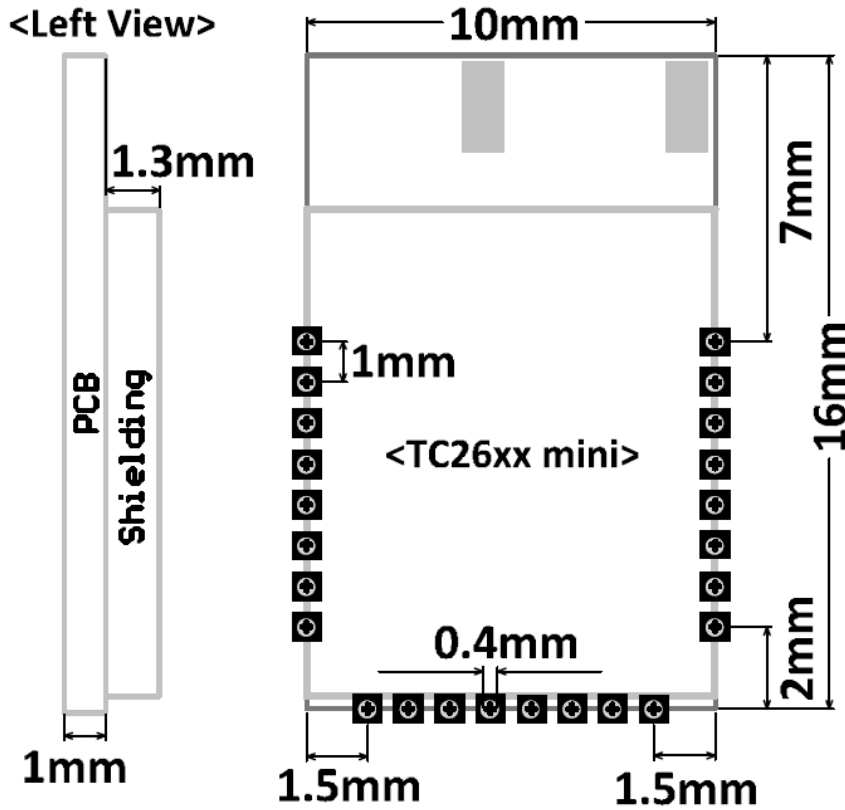
Example schematic:



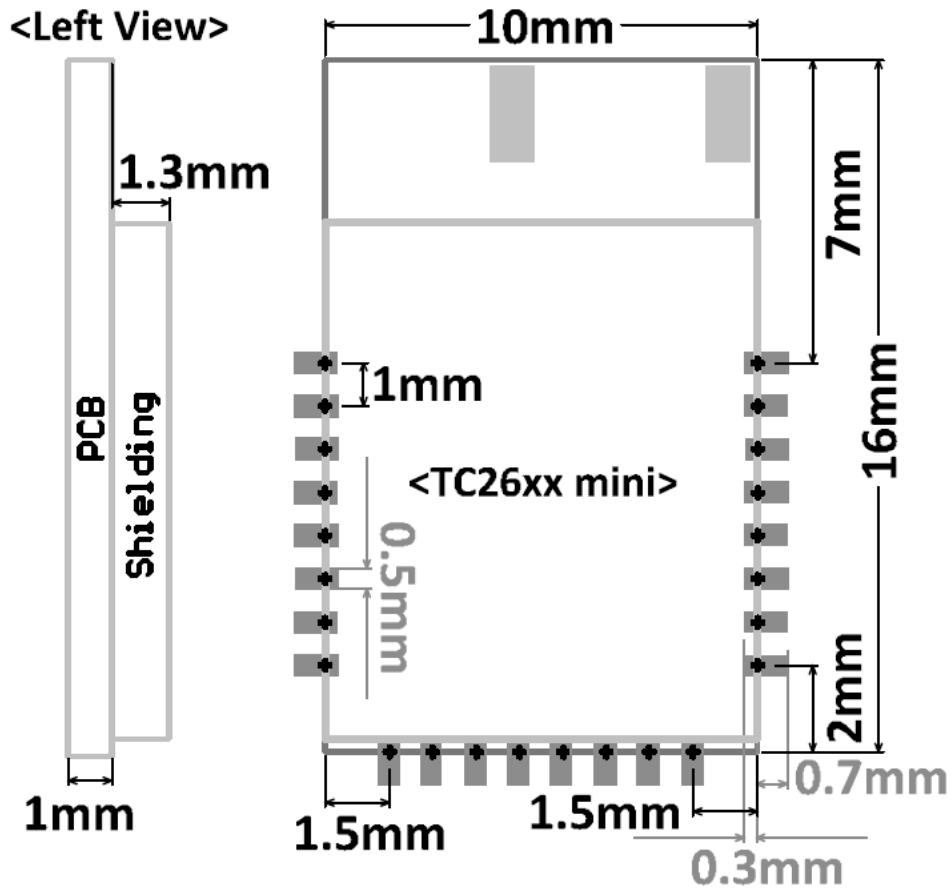
Example schematic (external antenna):



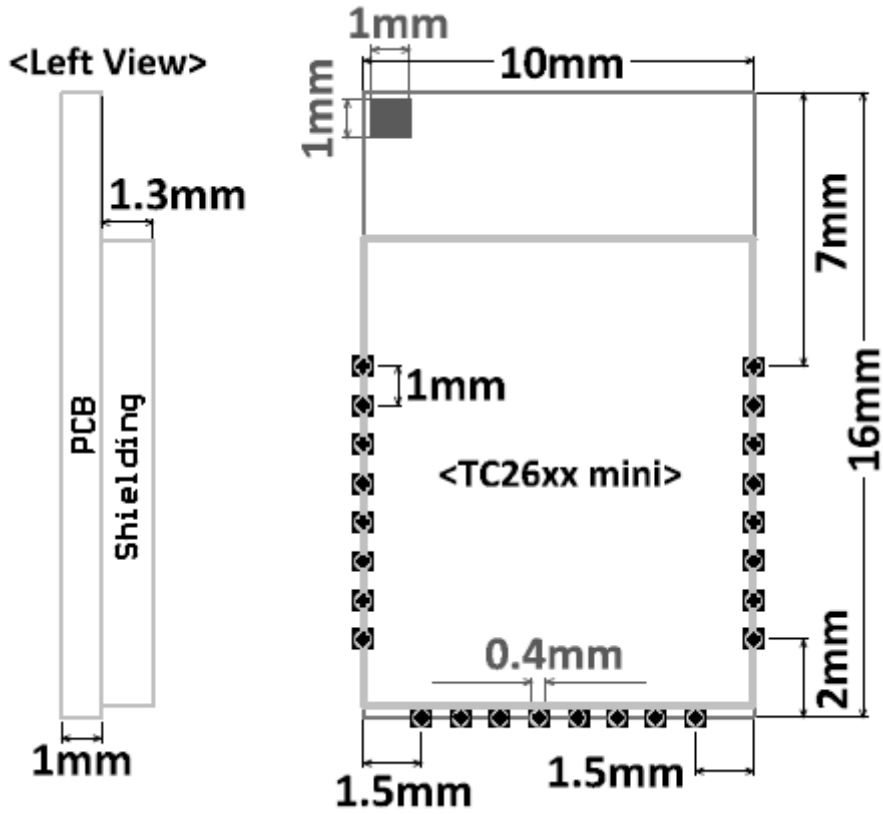
■ TC26x0-SF128-02-XA RF Module (internal antenna) Dimension



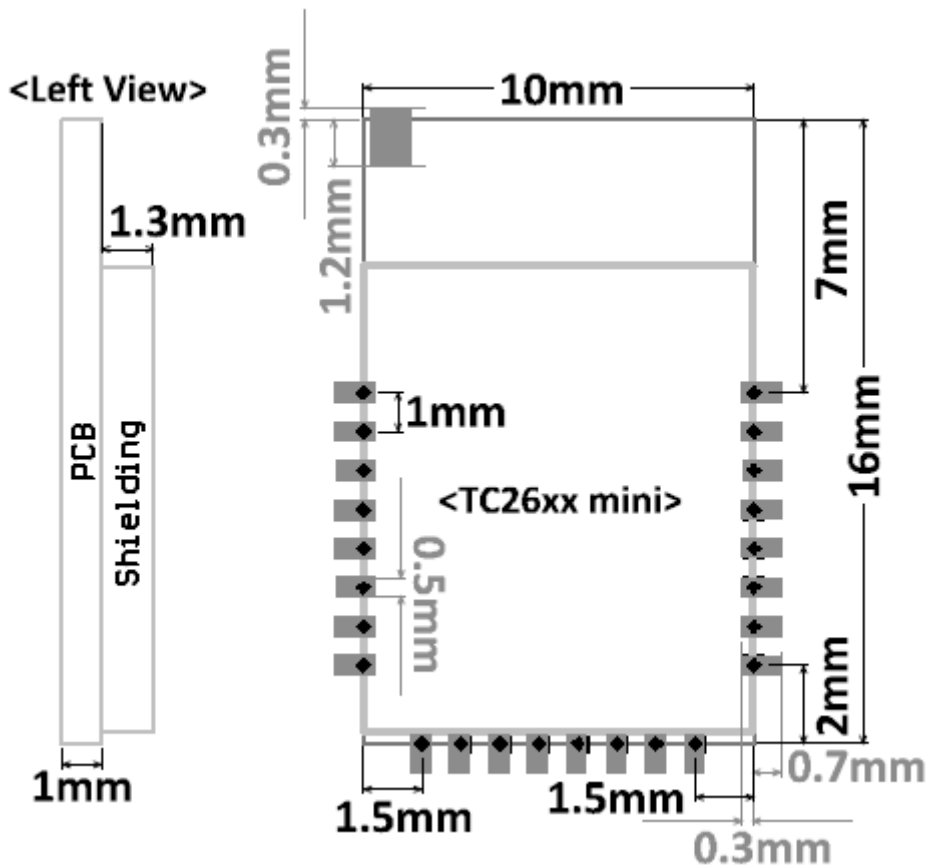
■ Recommended PCB layout for Module

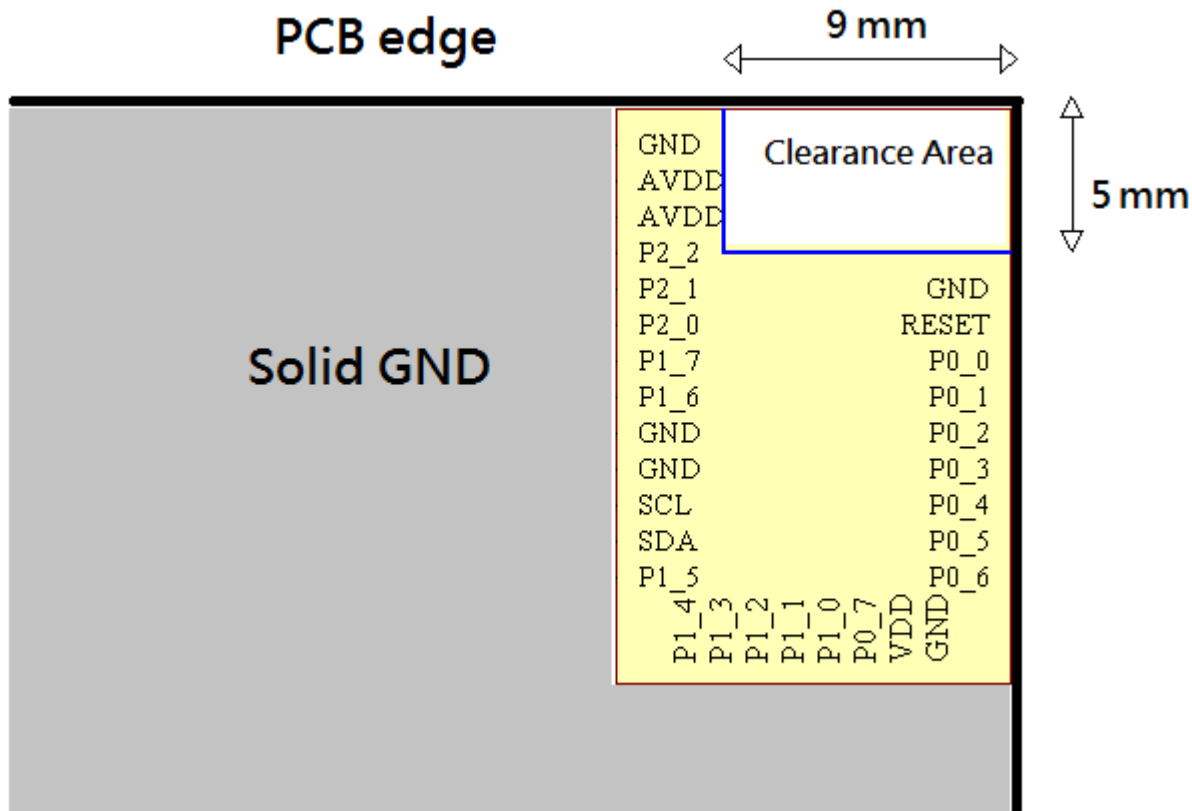


TC26x0-SF128-02-XE RF Module (external antenna) Dimension



Recommended PCB layout for Module





■ Document History

Revision	Date	Description/Changes
1.0	2015.07.08	First release
2.0	2016.04.08	Update drawing: PCB Size & layout guide
3.0	2016.5.27	Update external antenna information
3.1	2016.5.27	Add External Antenna PAD

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